

FPGA Tile 20

FIG. 1

20071129.021502

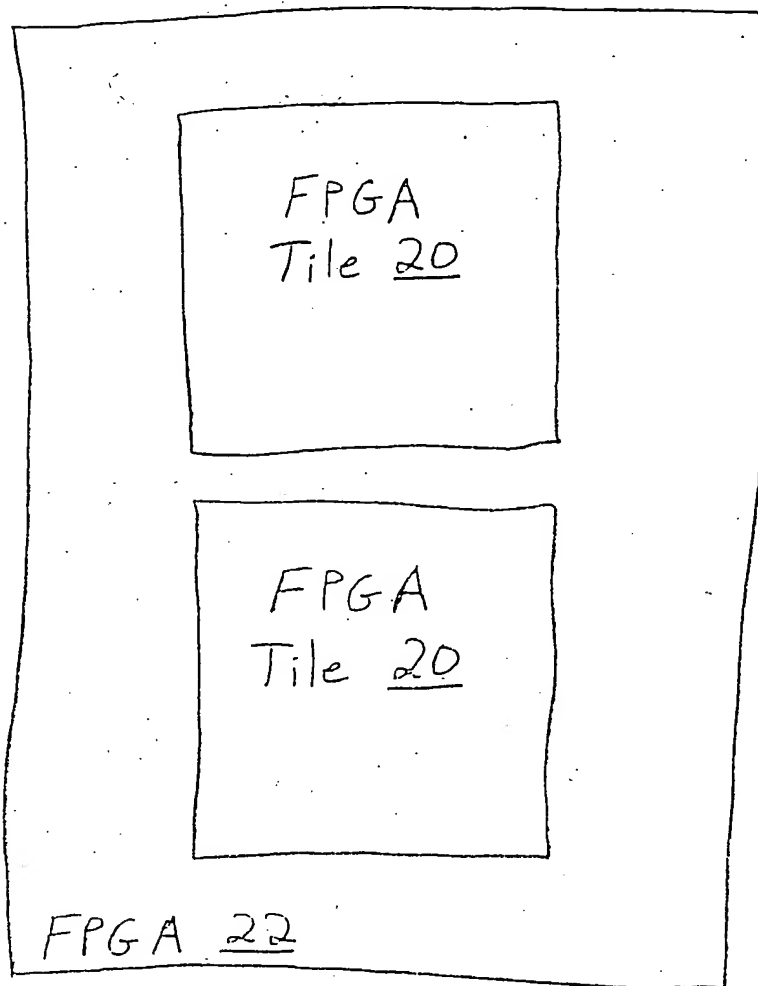


FIG. 2

205T20-68T200T

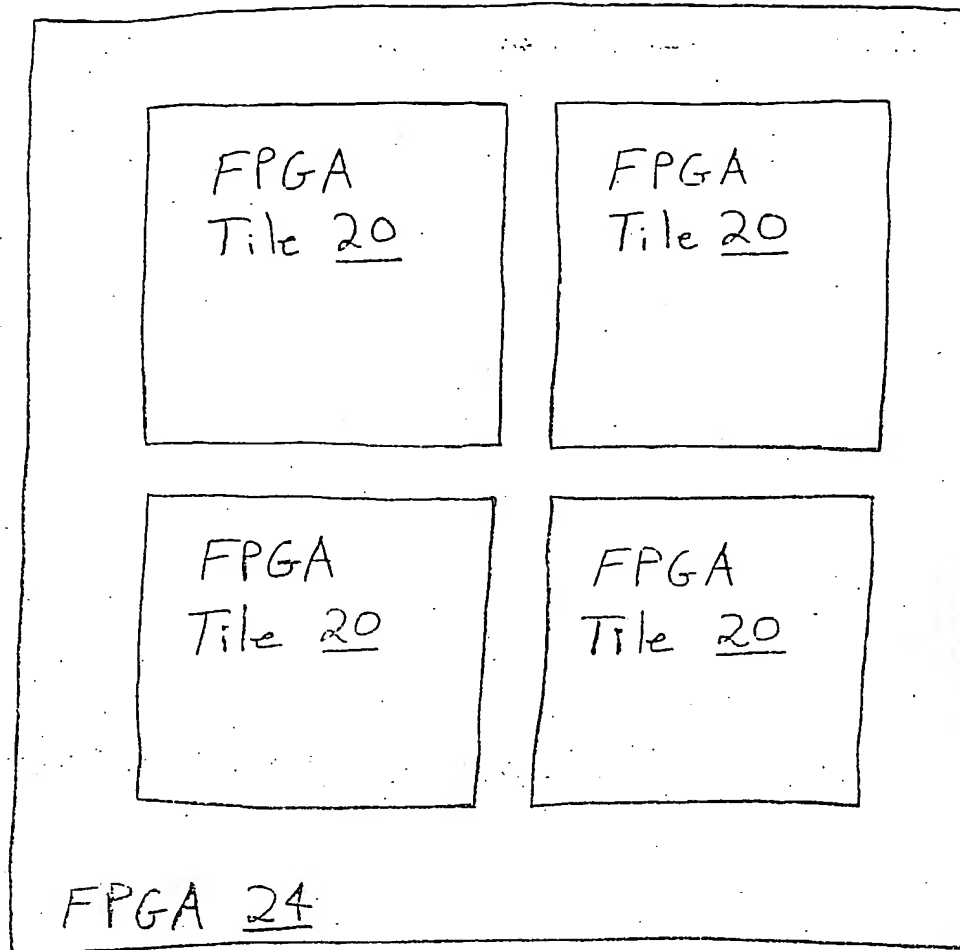


FIG. 3A

205T20" 68T400T

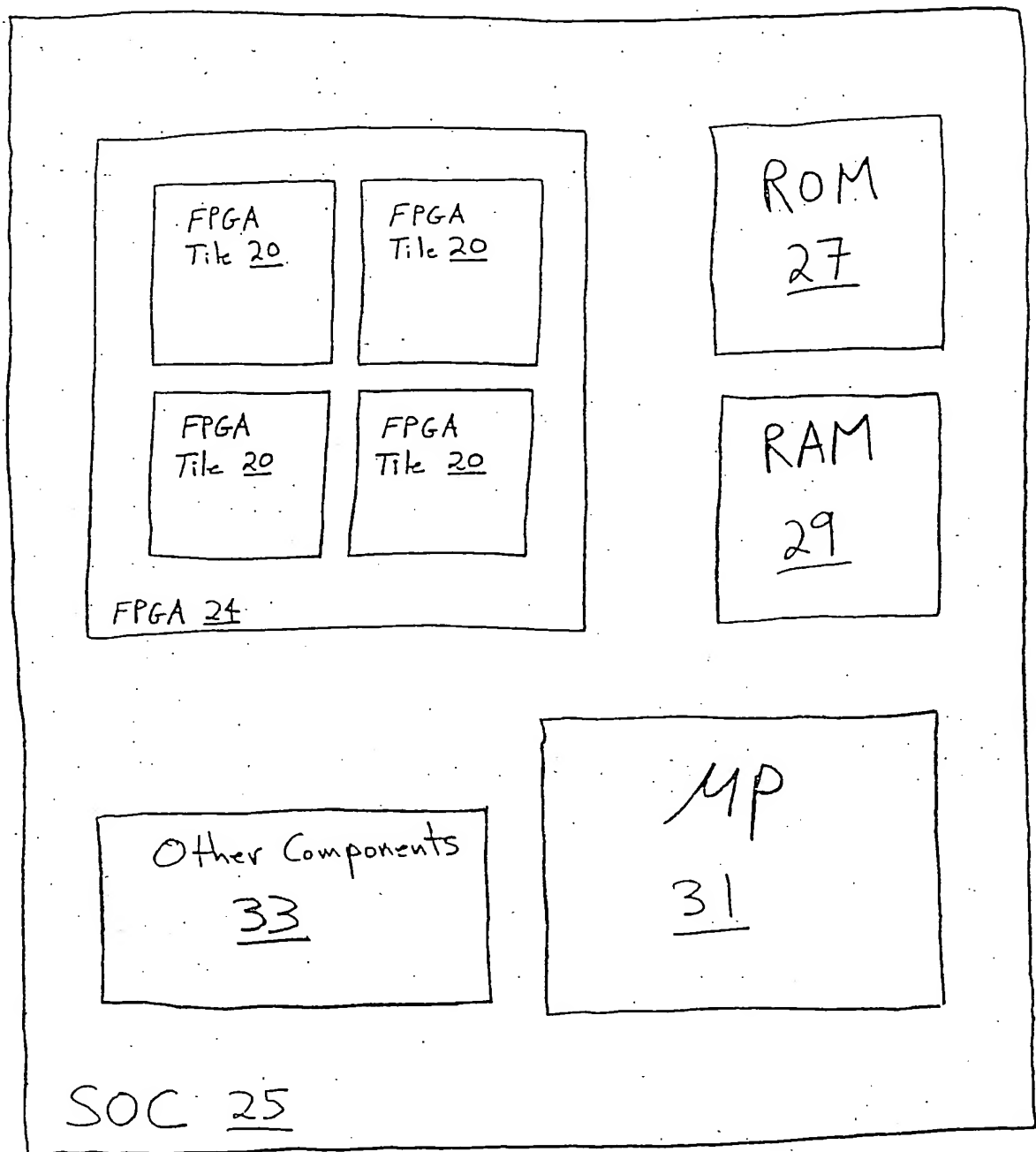


FIG. 3B

2007189-021500

205T20" 6BT2200T

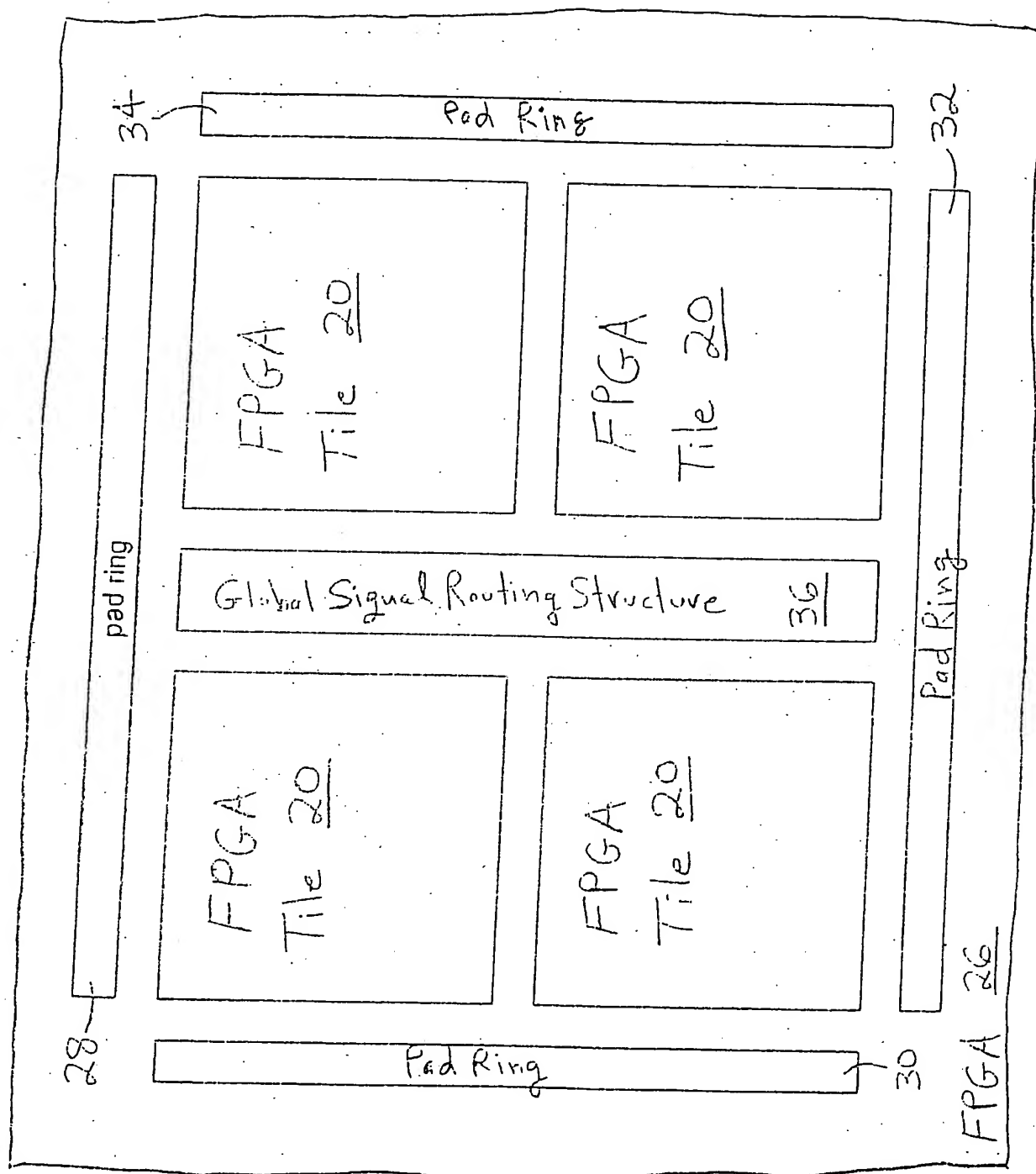


FIG 4

2057139-021502

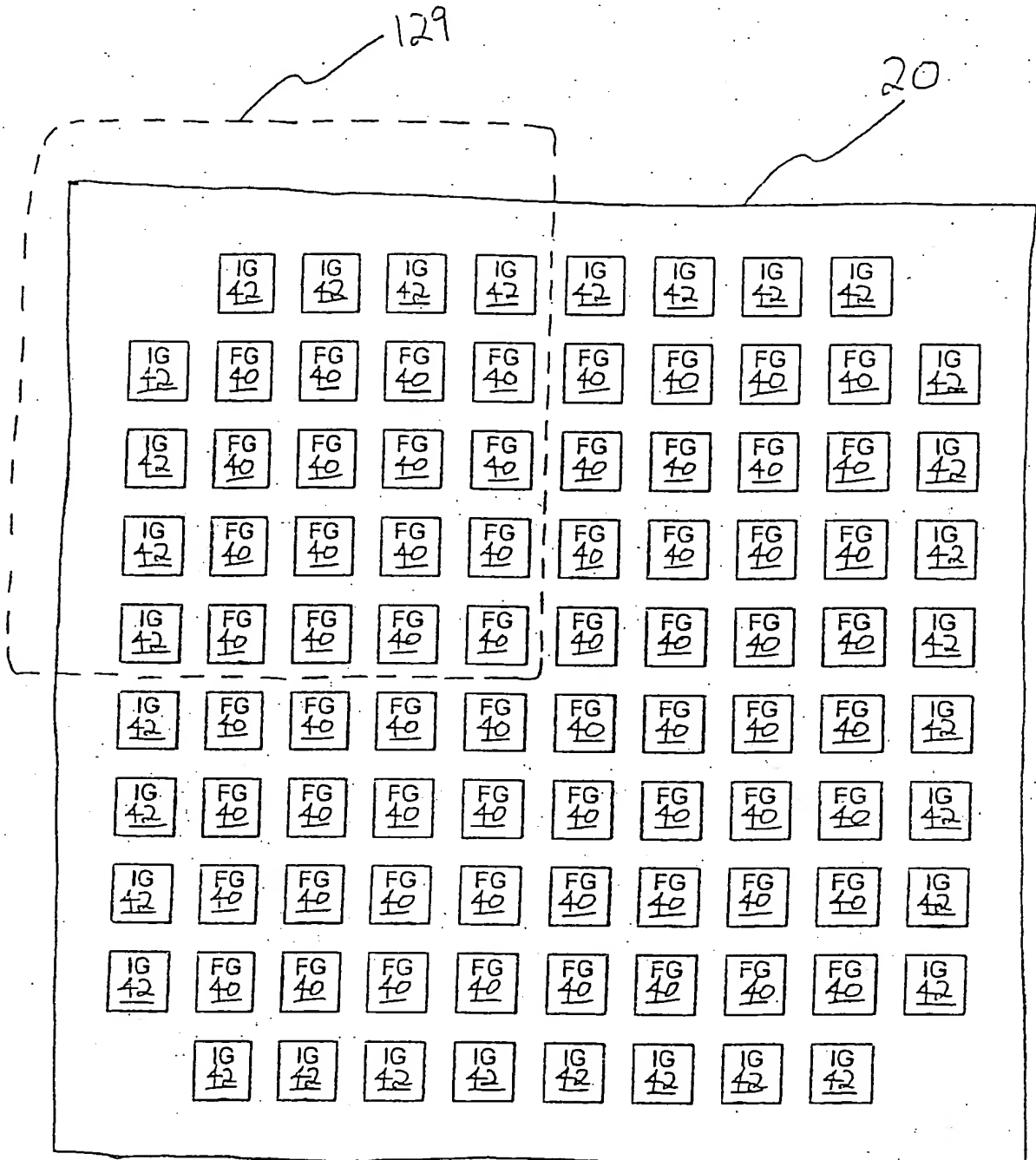


FIG. 5

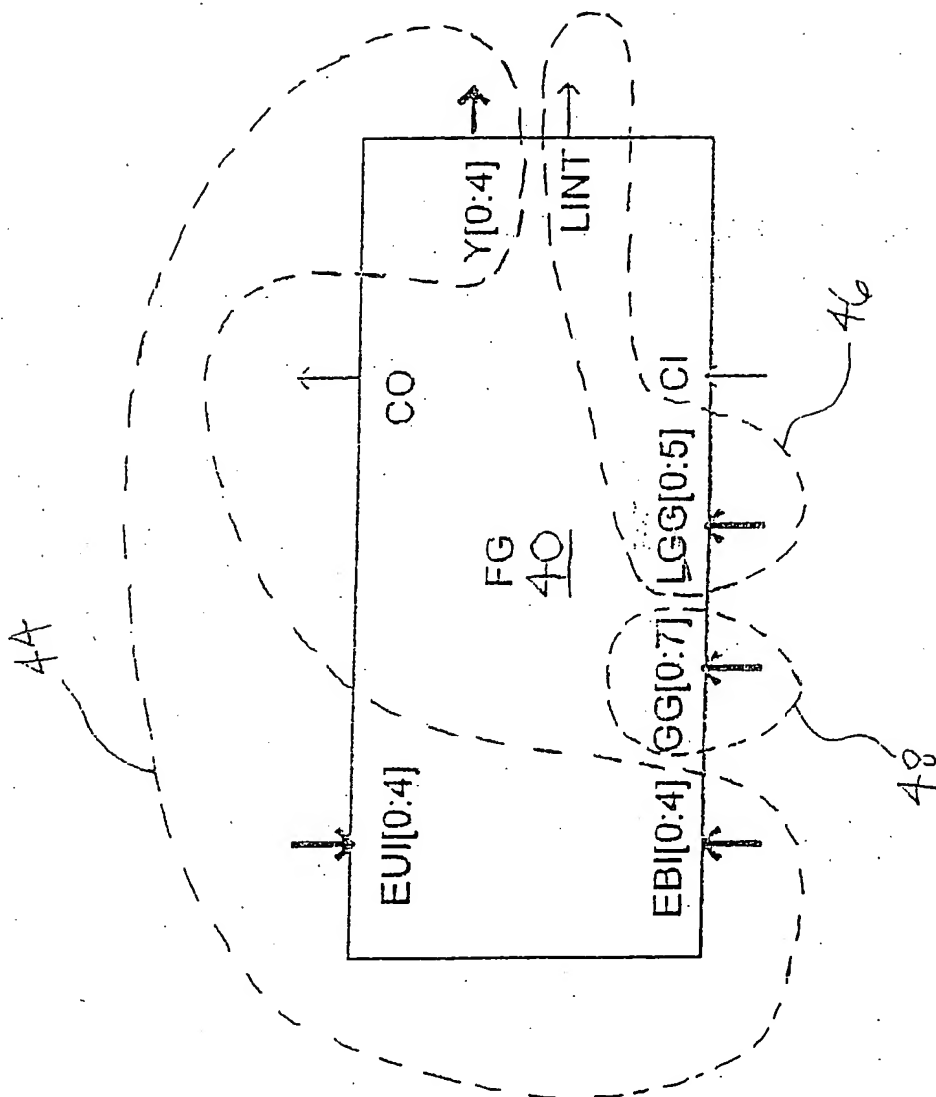


FIG. 6

FIG 7

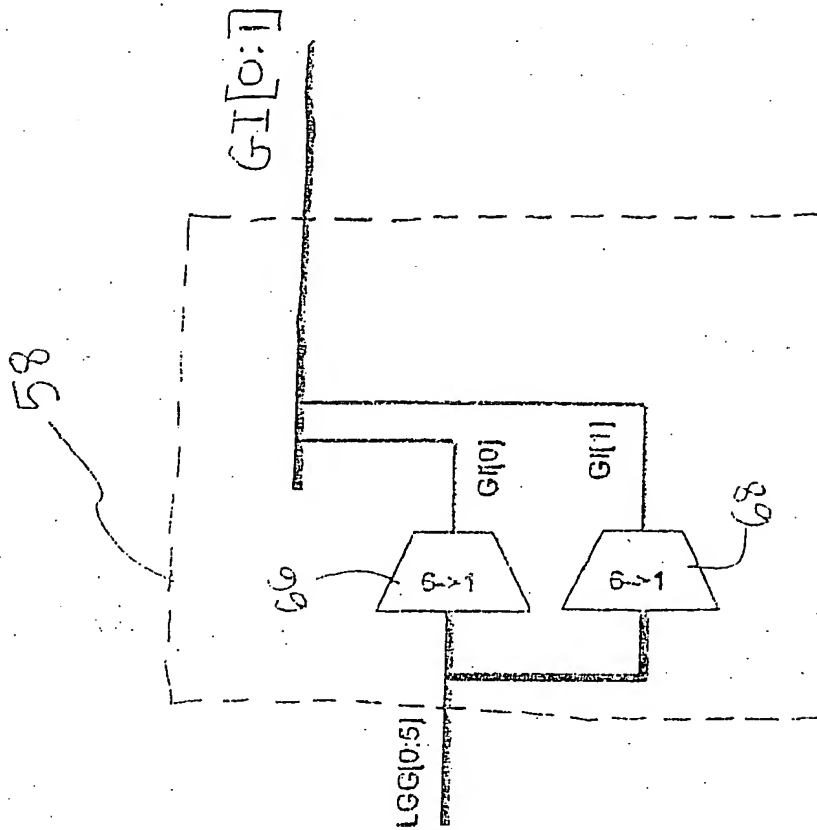


FIG. 8

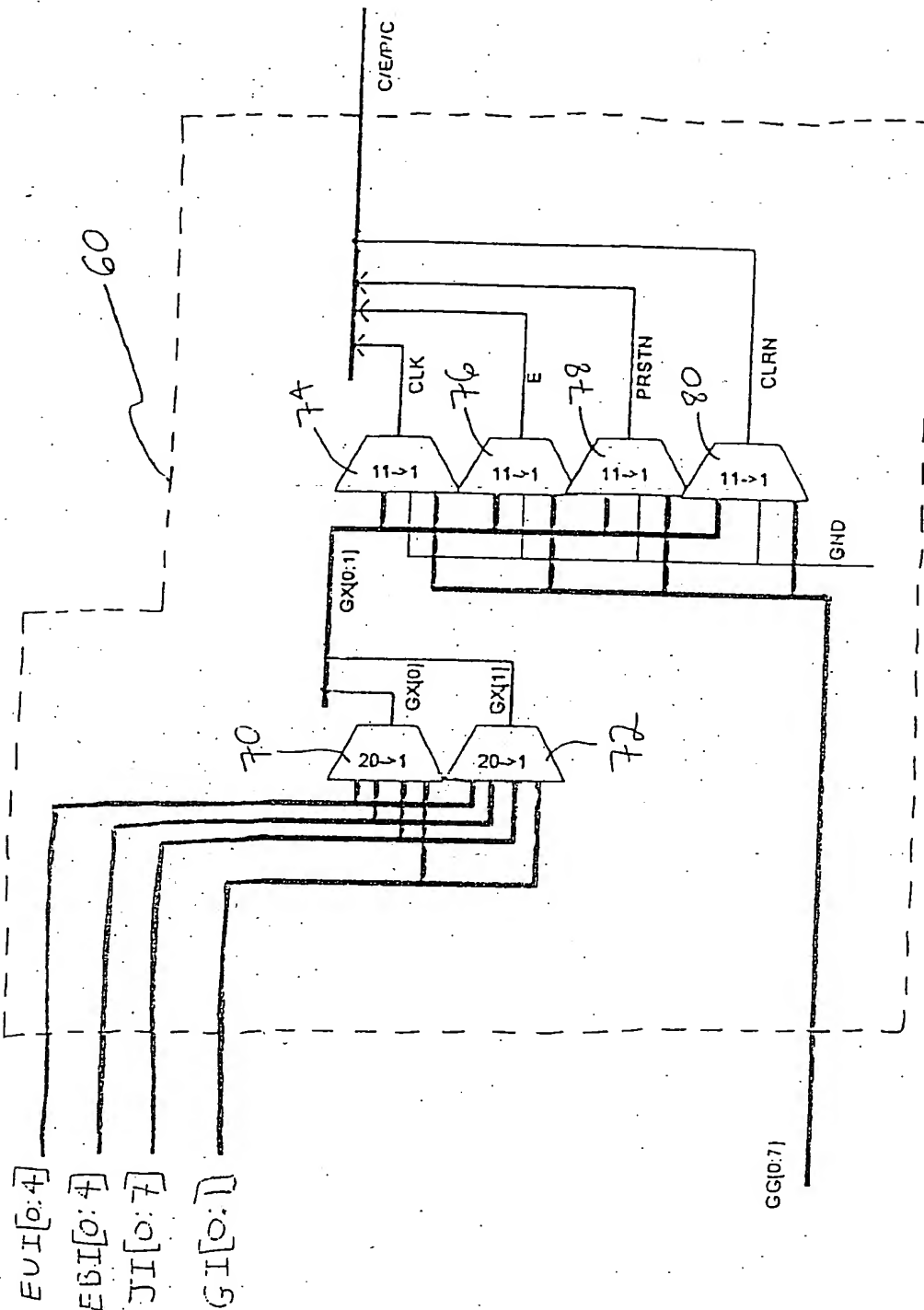


FIG. 9

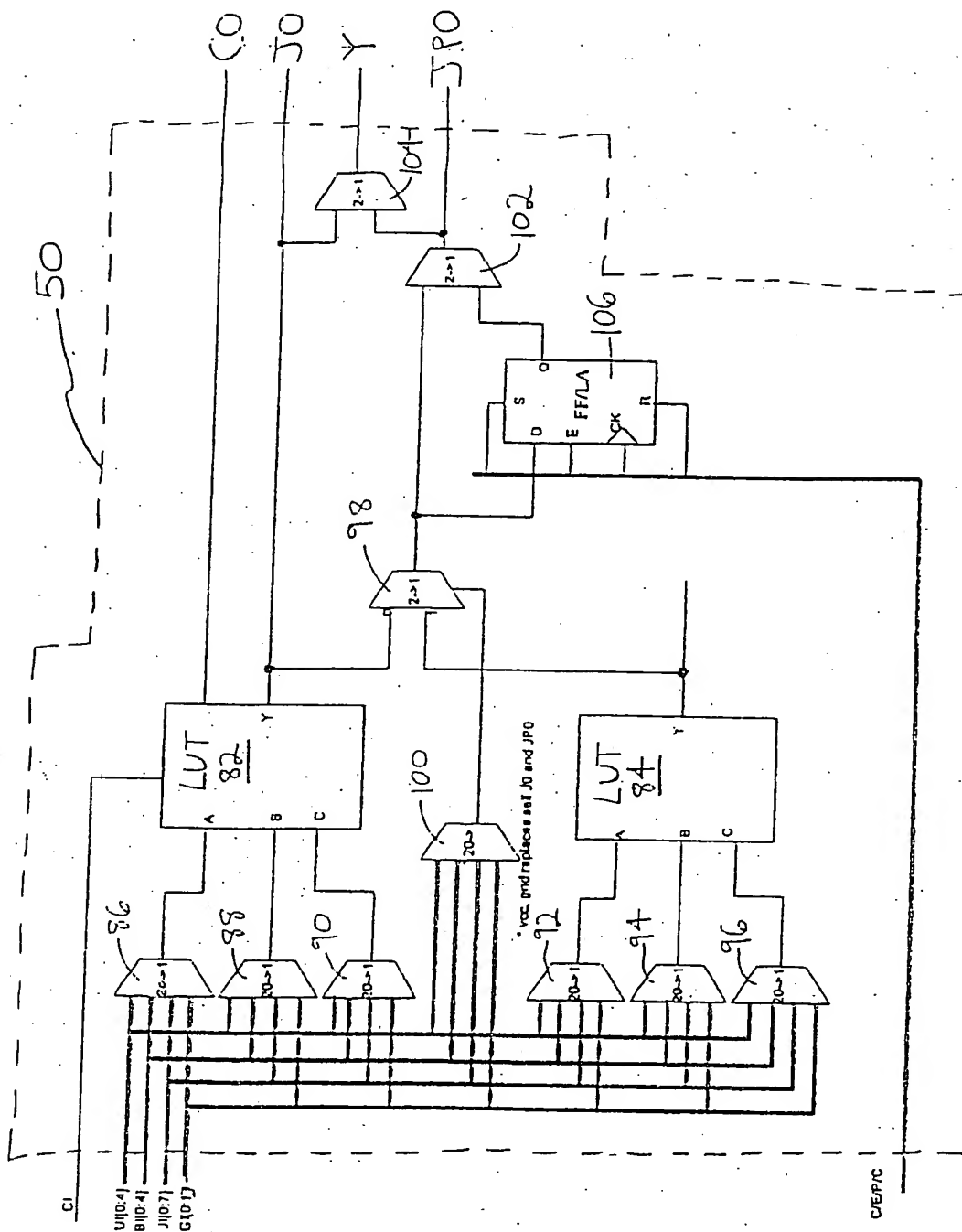


FIG. 10

205120" 6BT/200T

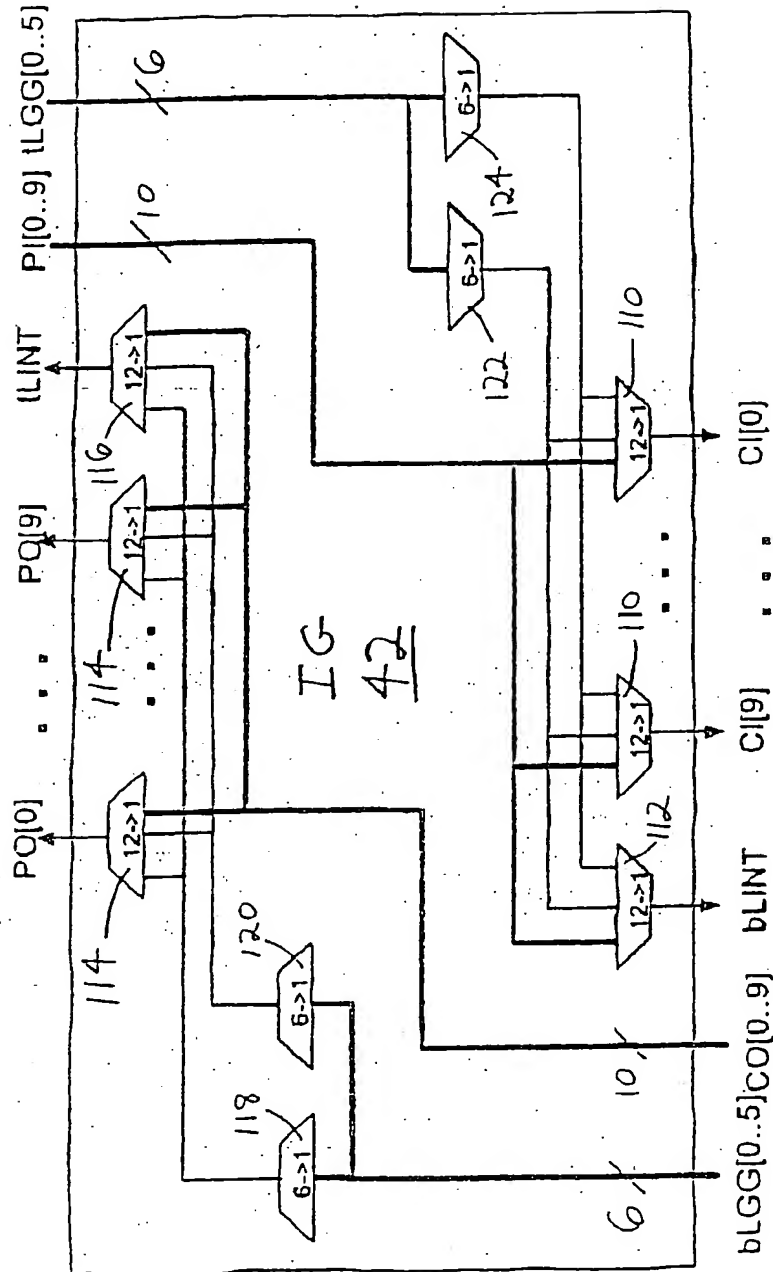


FIG. 11

205720" 68T 4400T

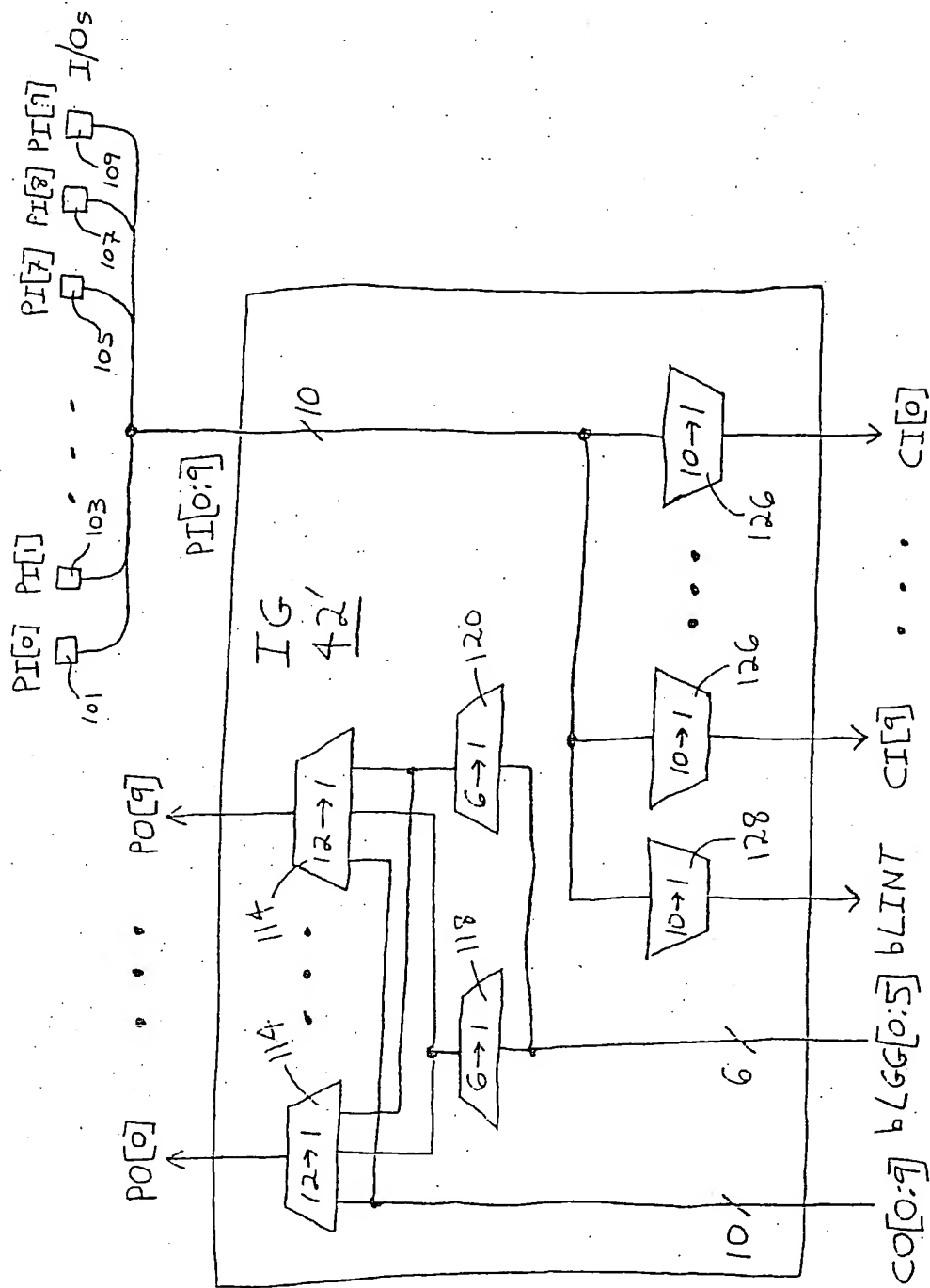
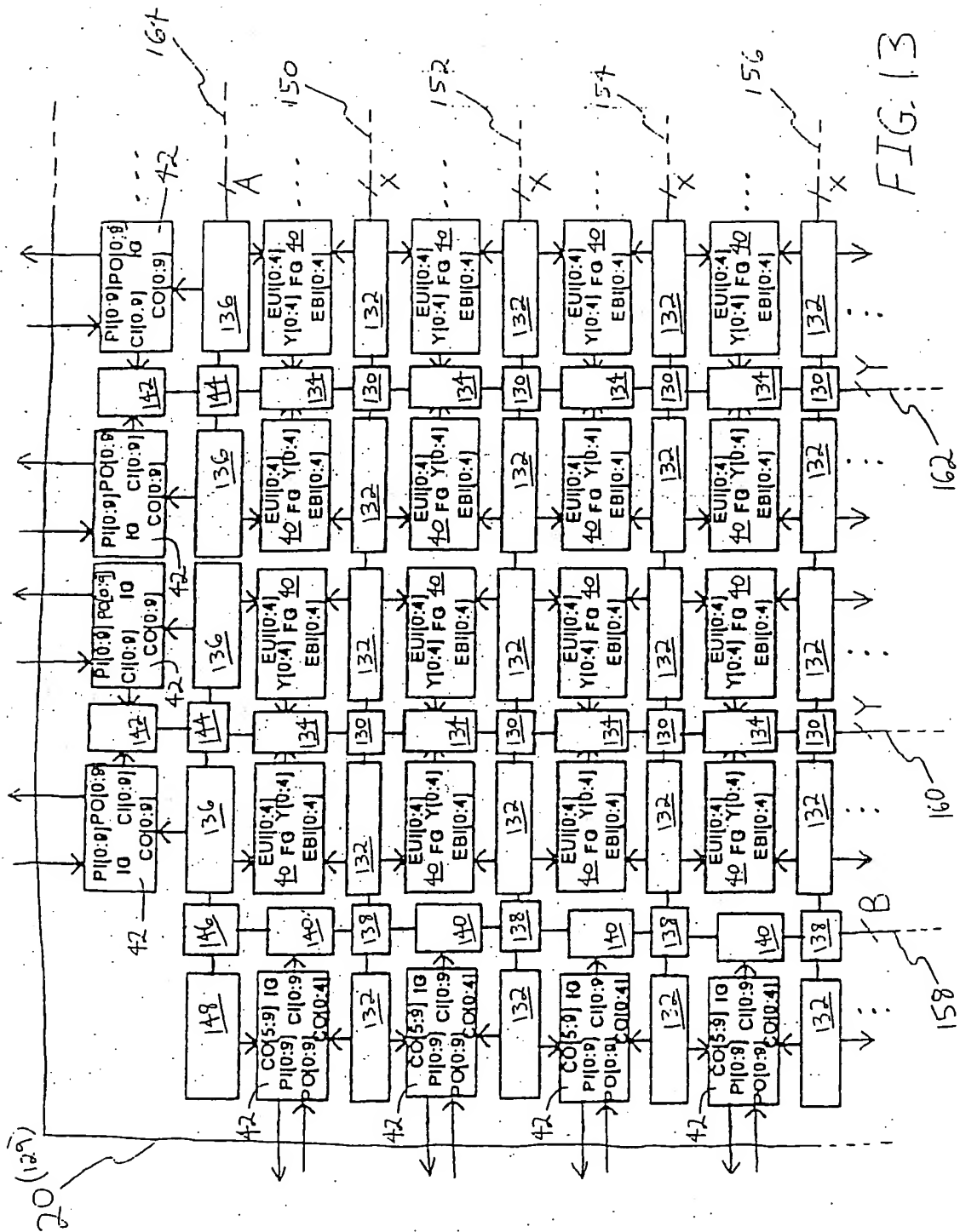
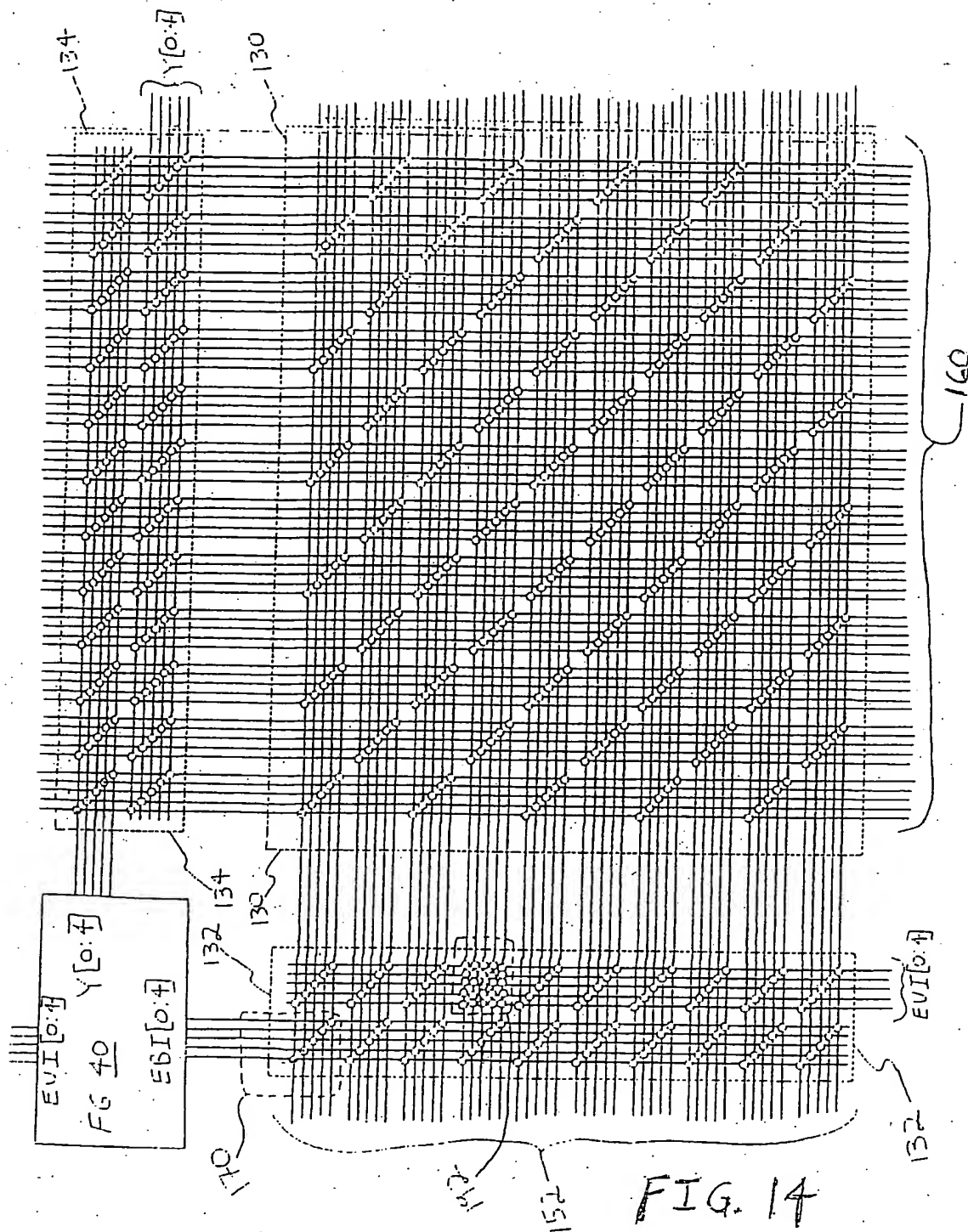


FIG. 12

205720" SET 200T



205F20*68F4400F



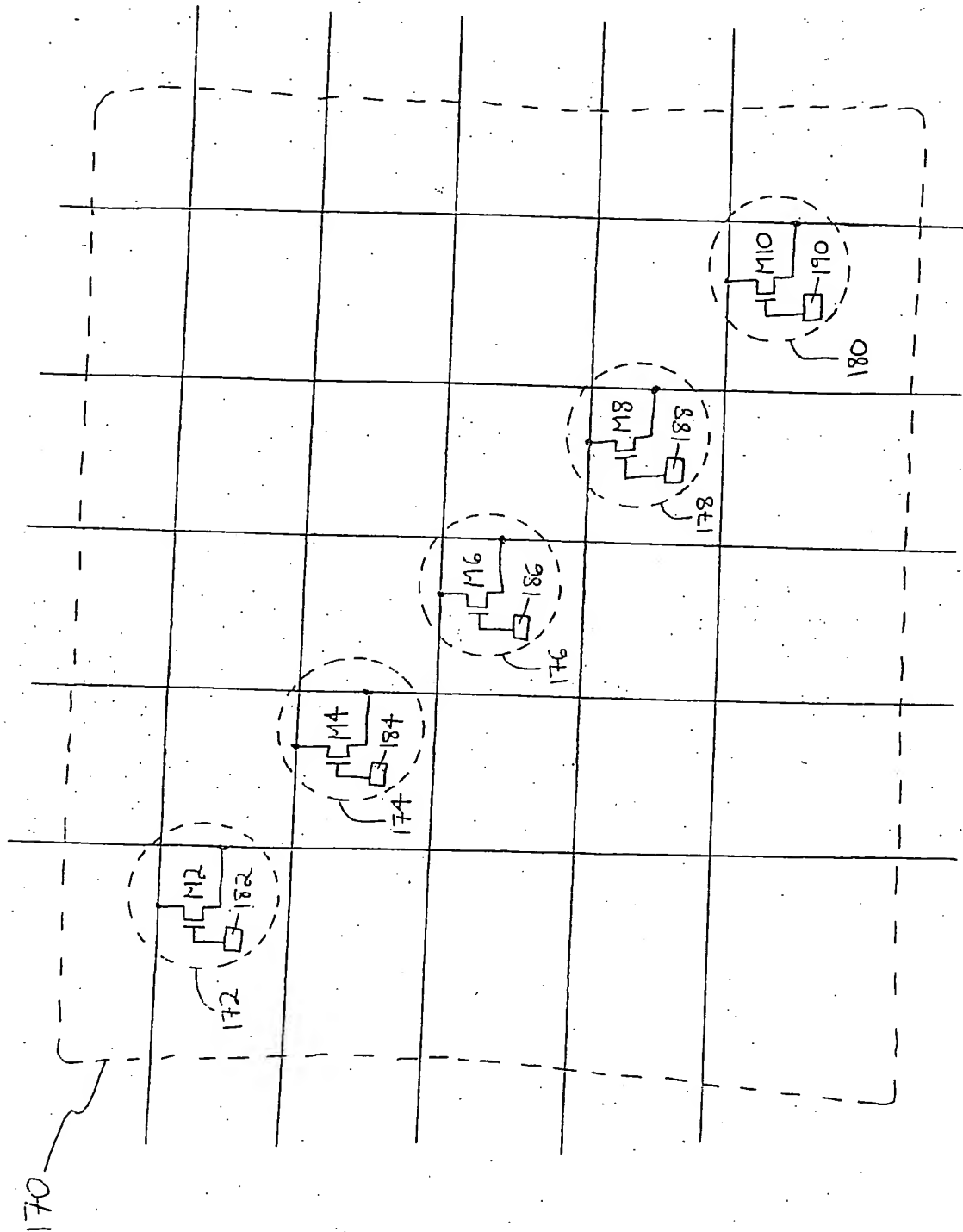


FIG. 15

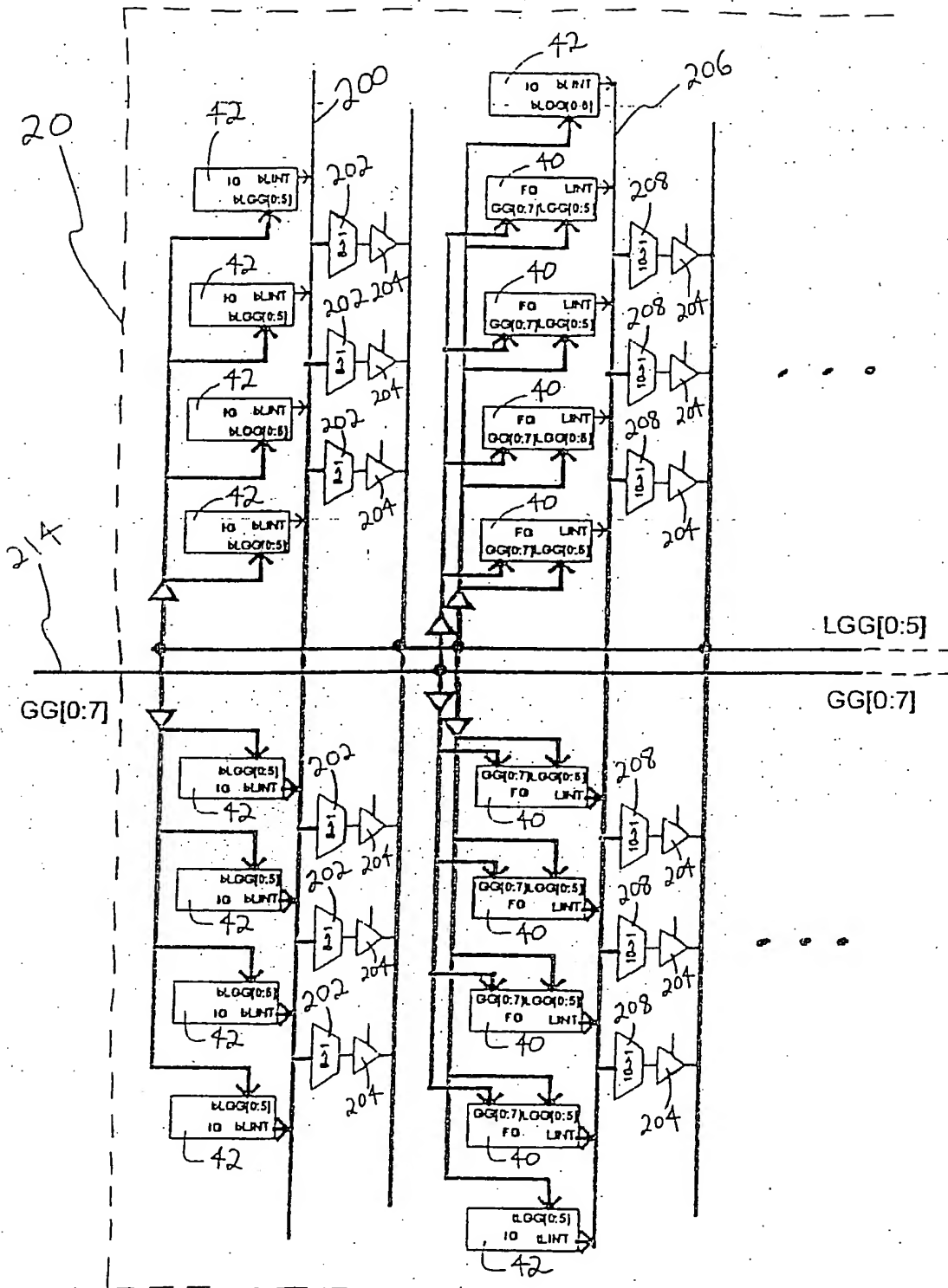


FIG. 16A

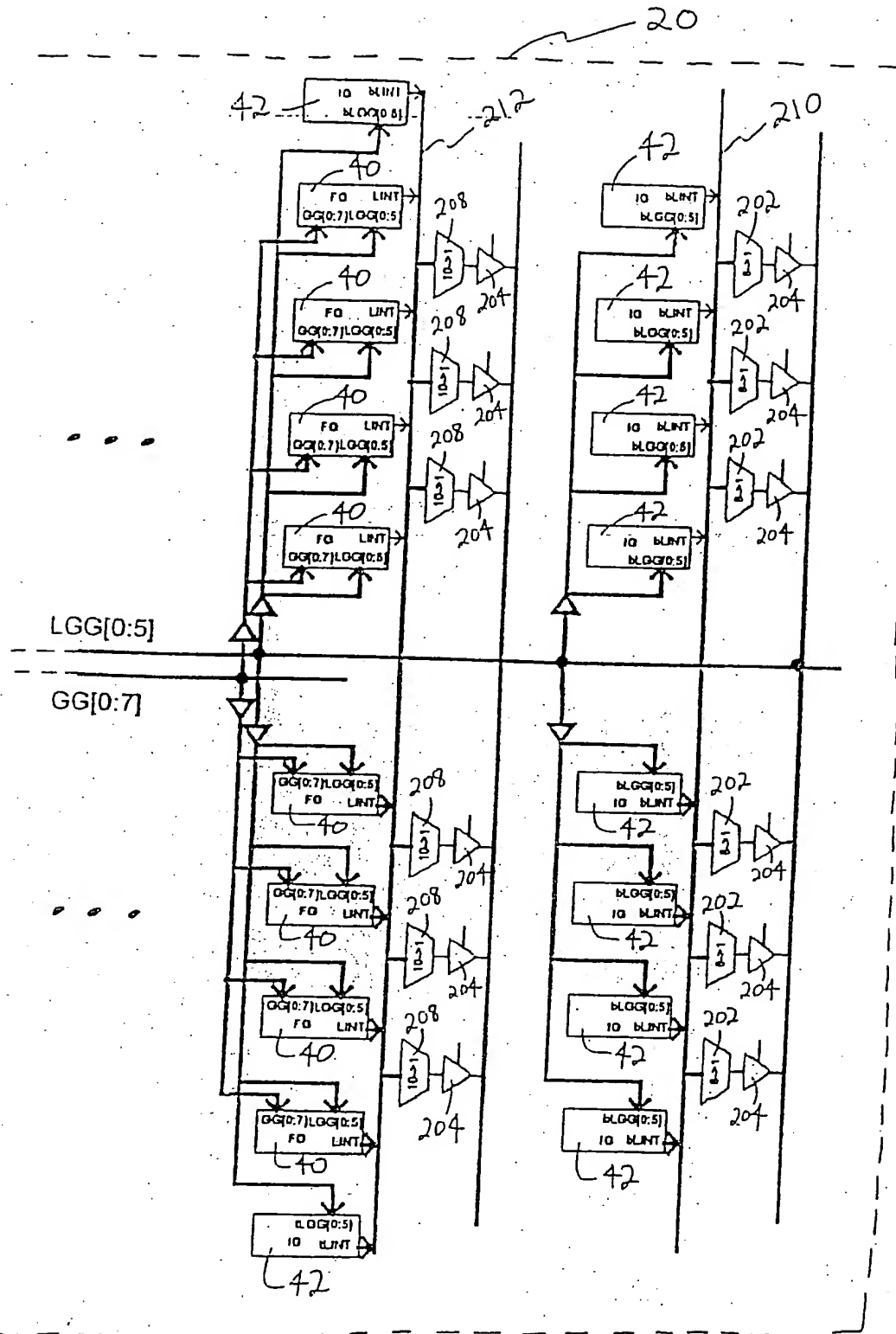


FIG. 16B

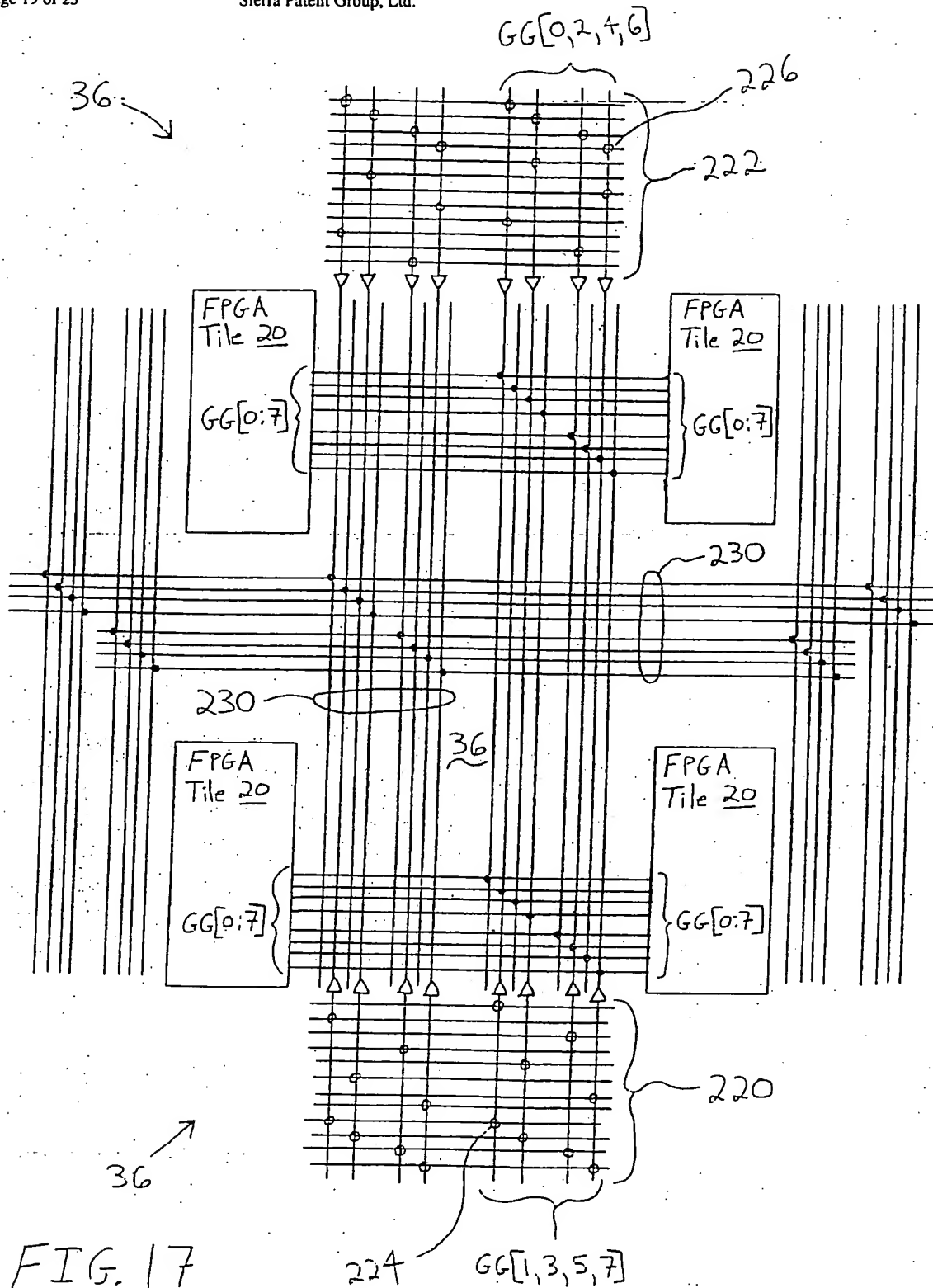


FIG. 17

205120" 68T/200T

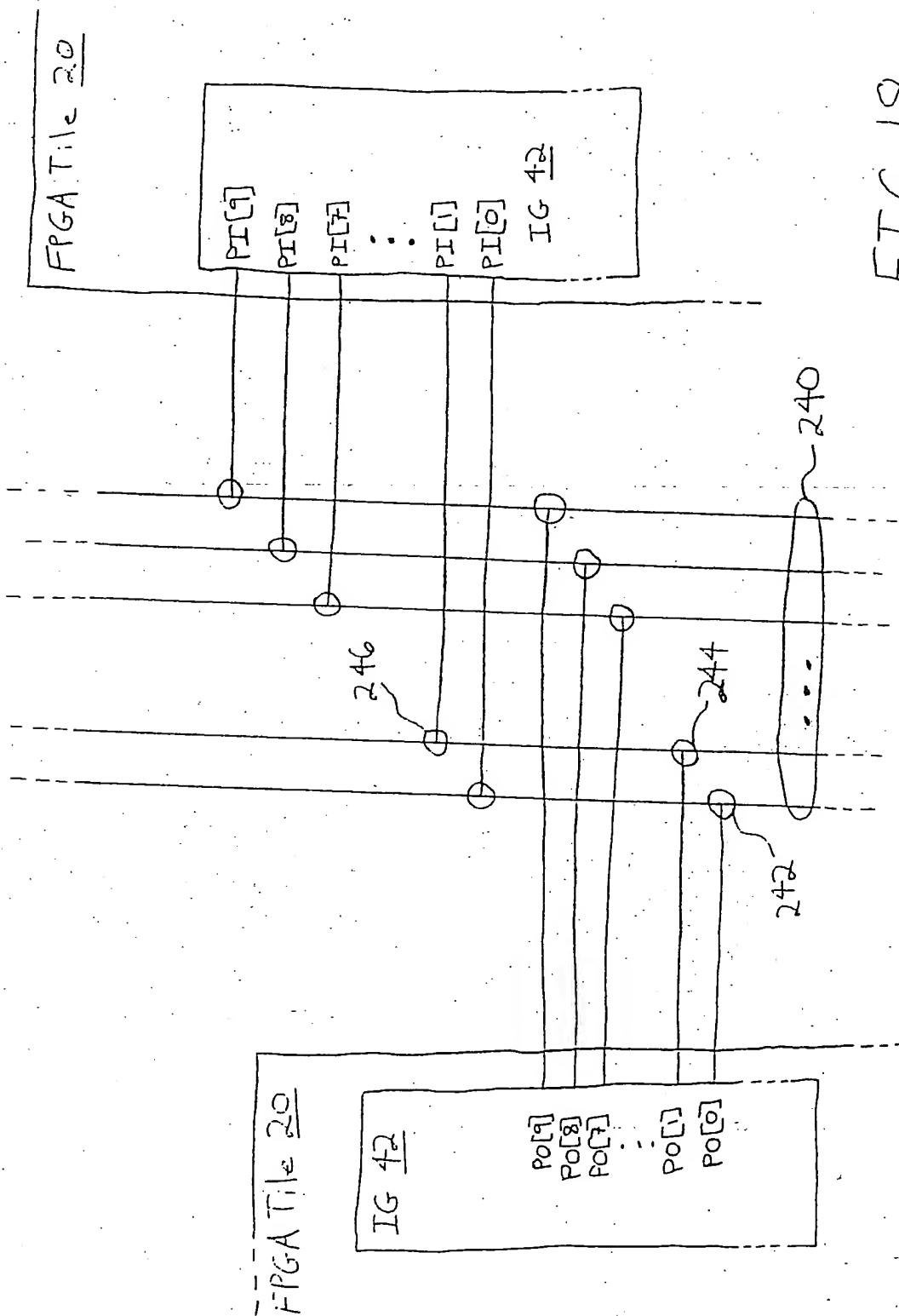


FIG. 18

FIG. 19

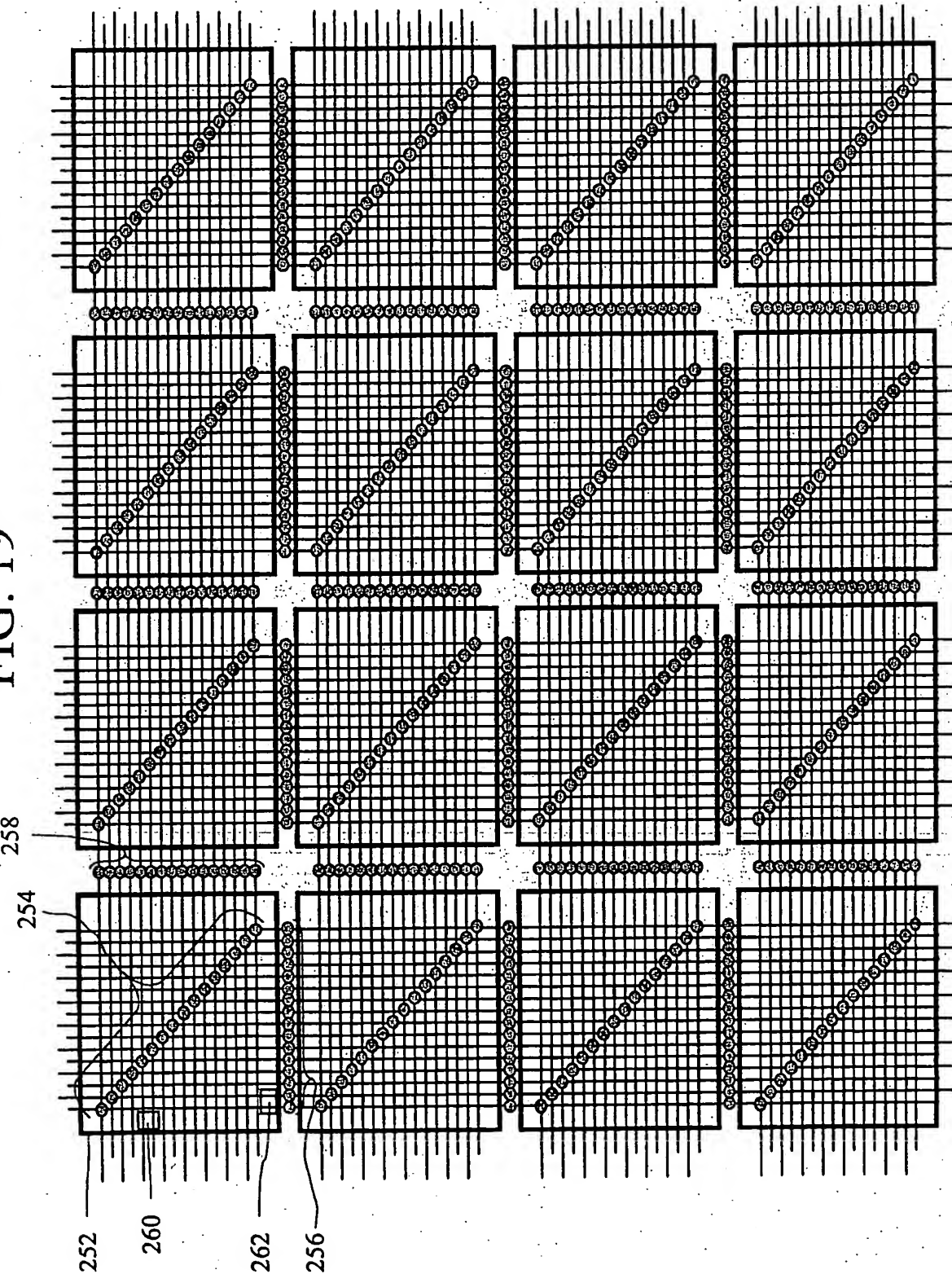
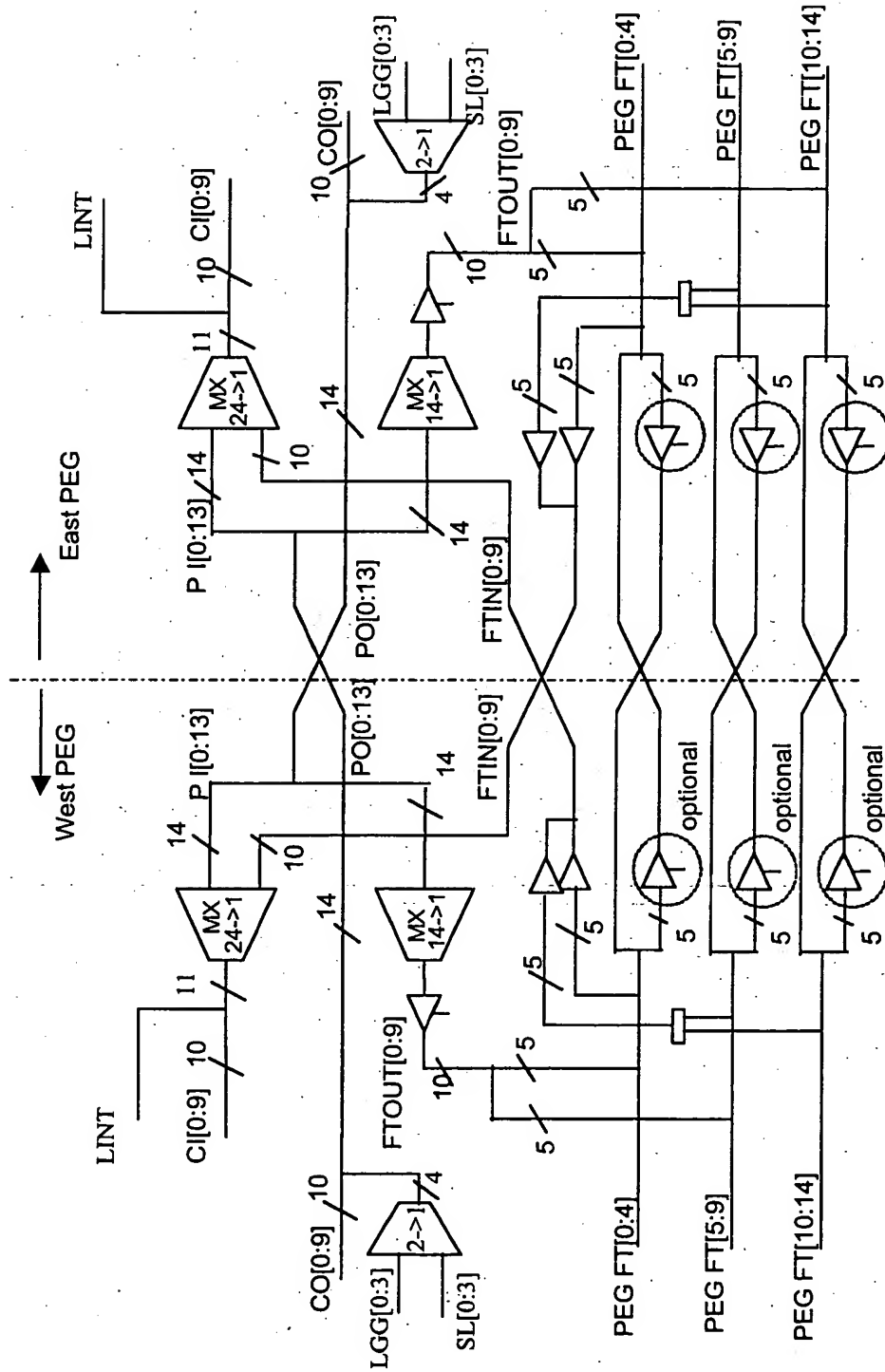


FIG. 20



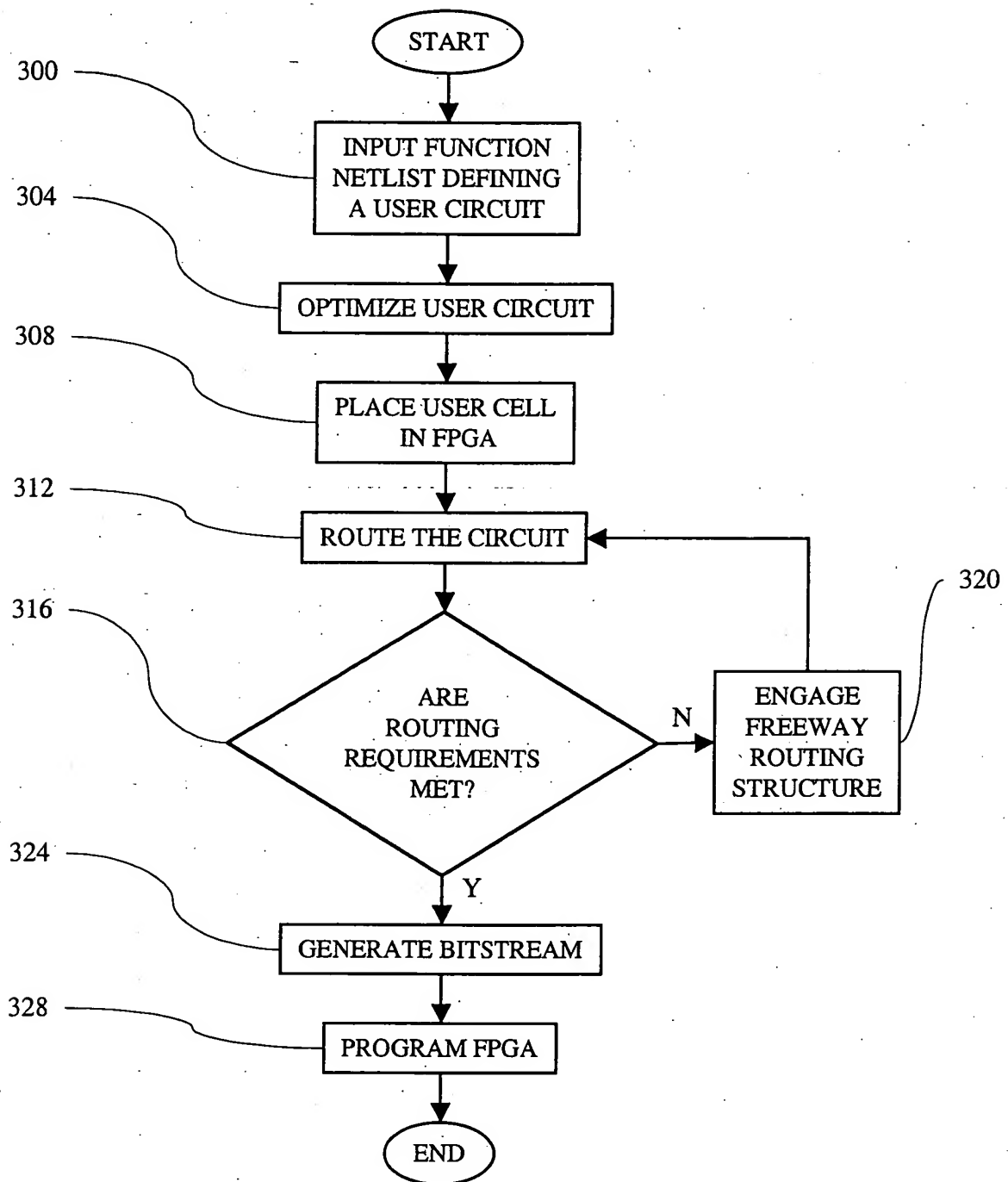


FIG. 21